



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in EC-VLSI

SEMESTER IV (2024-2028)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS		
			L	T	P		THEORY			PRACTICAL				
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess- ment* (20%)	End Sem University Exam (60%)	Teachers Assess- ment* (40%)			
1	BTEI401	Microprocessor and Microcontroller	3	0	2	4	60	20	20	30	20	150		
2	BTECIOT501	Communication Systems	3	0	2	4	60	20	20	30	20	150		
3	ML307	Environmental Management and Sustainability	4	0	0	4	60	20	20	0	0	100		
4	BTCS306M	Fundamentals of Data Structures	3	0	2	4	60	20	20	30	20	150		
5	BTEC614	Advanced Digital Design (HDL)	3	1	2	5	60	20	20	30	20	150		
6	-	Moocs	-	-	-	2	-	-	-	-	50	50		
7	BTEC405	Programming with Arduino	0	0	2	1	0	0	0	30	20	50		
TOTAL			16	1	10	24	300	100	100	150	150	800		

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

* Students have to compulsorily opt MOOCS course of two credit from Swayam Portal

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6/11/2026*
Chairperson
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Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

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Vishwavidyalaya, Indore

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Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

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Registrar
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